



1. Description

The KIA78L06 is monolithic fixed voltage regulator integrated circuit. It is suitable for applications that require supply current up to 100mA.

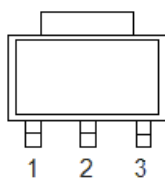
2. Features

- n Output current up to 100mA
- n No external part needed
- n Thermal overload shutdown protection
- n Short circuit current limiting
- n SOT89 package

3. Applications

- n Battery-powered circuitry
- n Post regulator for switching power supply

4. Pinning information



SOT-89 Front View

Pin	Description
1	V_{OUT}
2	GND
3	V_{IN}



5. Maximum ratings

(Ta=25°C, unless otherwise notes)

Parameter	Symbol	Rating	Units
Input voltage	V_{IN}	30	V
Power dissipation	P_D	500	mW
Junction temperature	T_J	-20~+125	°C
Operating temperature	T_{OPR}	-20~+85	°C
Storage temperature	T_{STG}	-65~+150	°C

6. Electrical characteristics

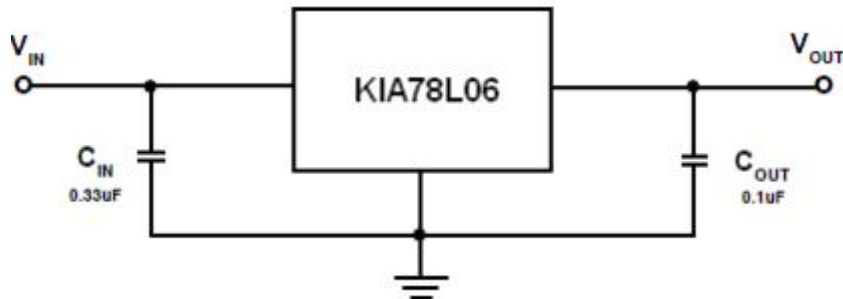
($V_{IN}=11V$, $I_{OUT}=40mA$, $C_{IN}=0.33\mu F$, $C_{OUT}=0.1\mu F$, $T_J=25^\circ C$, unless otherwise notes)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Output voltage	V_{OUT}		5.76	6.0	6.24	V
		$8.1V \leq V_{IN} \leq 21V$ $1.0mA \leq I_{OUT} \leq 40mA$	5.70	6.0	6.30	V
		$1.0mA \leq I_{OUT} \leq 70mA$	5.58	6.0	6.42	V
Line regulation	Reg line	$8.1V \leq V_{IN} \leq 21V$	-	50	150	mV
		$9.0V \leq V_{IN} \leq 21V$	-	45	110	mV
Load regulation	Reg load	$1.0mA \leq I_{OUT} \leq 100mA$	-	12	70	mV
		$1.0mA \leq I_{OUT} \leq 40mA$	-	5.5	35	mV
Quiescent current	I_Q		-	3.1	6.0	mA
Quiescent current change	ΔI_Q	$9.0V \leq V_{IN} \leq 20V$	-	0.15	1.5	mA
		$1.0mA \leq I_{OUT} \leq 40mA$	-	0.08	0.1	mA
Output noise voltage	V_{ON}	$10Hz \leq f \leq 100KHz$	-	40	-	μV_{rms}
Ripple rejection ratio	RR	$10V \leq V_{IN} \leq 20V$, $f=120Hz$	39	47	-	dB
Dropout voltage	V_D		-	1.7	-	V

Note1: The maximum steady state usable output current is dependent on input voltage, heat sinking, lead length of the package and copper pattern of PCB.



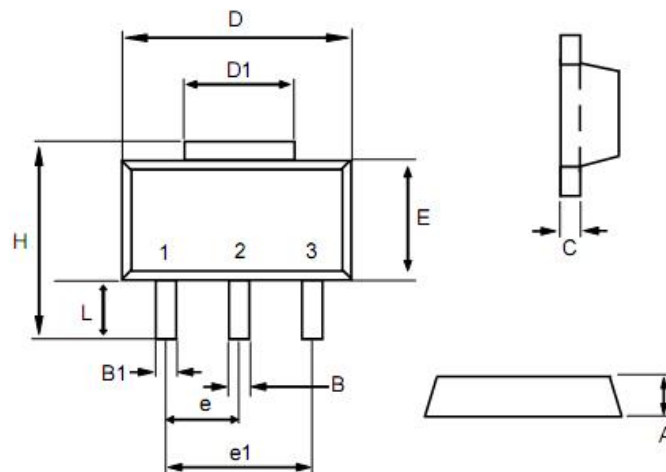
7. Application circuits



Note1: The input voltage must remain typically 1.7V above the output voltage.

Note2: Bypass capacitors are recommended for optimum stability and transient response and should be located as close as possible to the regulators.

8. Package outline



Dim	min	max
A	1.40	1.60
B	0.40	0.56
B1	0.35	0.48
C	0.35	0.44
D	4.40	4.60
D1	1.35	1.83
e	1.50 BSC	
e1	3.00 BSC	
E	2.29	2.60
H	3.75	4.25
L	0.80	1.20



1.Description

The KIA78L06 is monolithic fixed voltage regulator integrated circuit. It is suitable for applications that require supply current up to 100mA.

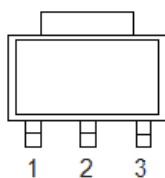
2.Features

- n Output current up to 100mA
- n No external part needed
- n Thermal overload shutdown protection
- n Short circuit current limiting
- n SOT89 package

3.Applications

- n Battery-powered circuitry
- n Post regulator for switching power supply

4. Pinning information



SOT-89 Front View

Pin	Description
1	V_{OUT}
2	GND
3	V_{IN}



5. Maximum ratings

(Ta=25°C, unless otherwise notes)

Parameter	Symbol	Rating	Units
Input voltage	V_{IN}	30	V
Power dissipation	P_D	500	mW
Junction temperature	T_J	-20~+125	°C
Operating temperature	T_{OPR}	-20~+85	°C
Storage temperature	T_{STG}	-65~+150	°C

6. Electrical characteristics

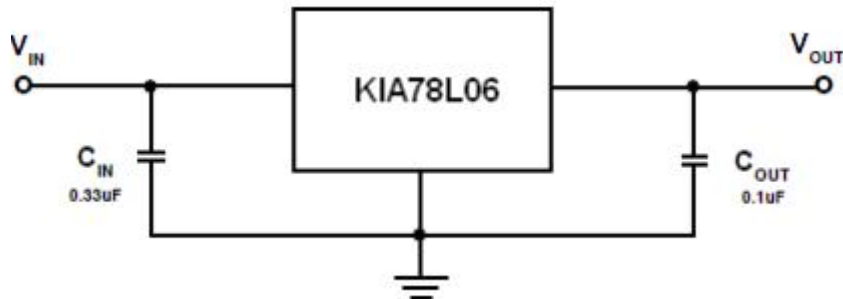
($V_{IN}=11V$, $I_{OUT}=40mA$, $C_{IN}=0.33\mu F$, $C_{OUT}=0.1\mu F$, $T_J=25^\circ C$, unless otherwise notes)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Output voltage	V_{OUT}		5.76	6.0	6.24	V
		$8.1V \leq V_{IN} \leq 21V$ $1.0mA \leq I_{OUT} \leq 40mA$	5.70	6.0	6.30	V
		$1.0mA \leq I_{OUT} \leq 70mA$	5.58	6.0	6.42	V
Line regulation	Reg line	$8.1V \leq V_{IN} \leq 21V$	-	50	150	mV
		$9.0V \leq V_{IN} \leq 21V$	-	45	110	mV
Load regulation	Reg load	$1.0mA \leq I_{OUT} \leq 100mA$	-	12	70	mV
		$1.0mA \leq I_{OUT} \leq 40mA$	-	5.5	35	mV
Quiescent current	I_Q		-	3.1	6.0	mA
Quiescent current change	ΔI_Q	$9.0V \leq V_{IN} \leq 20V$	-	0.15	1.5	mA
		$1.0mA \leq I_{OUT} \leq 40mA$	-	0.08	0.1	mA
Output noise voltage	V_{ON}	$10Hz \leq f \leq 100KHz$	-	40	-	μV_{rms}
Ripple rejection ratio	RR	$10V \leq V_{IN} \leq 20V$, $f=120Hz$	39	47	-	dB
Dropout voltage	V_D		-	1.7	-	V

Note1: The maximum steady state usable output current is dependent on input voltage, heat sinking, lead length of the package and copper pattern of PCB.



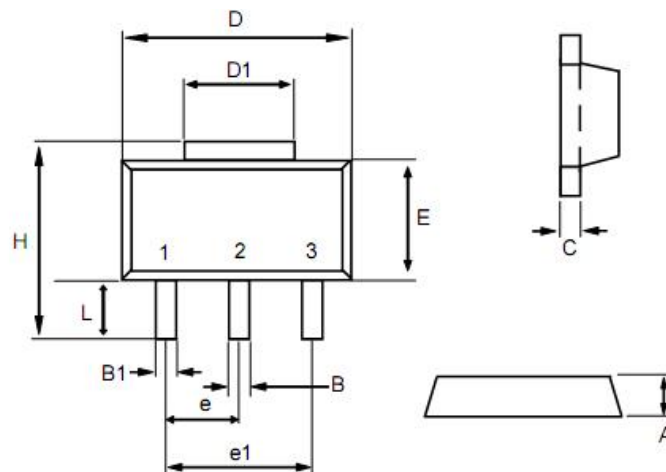
7. Application circuits



Note1: The input voltage must remain typically 1.7V above the output voltage.

Note2: Bypass capacitors are recommended for optimum stability and transient response and should be located as close as possible to the regulators.

8. Package outline



Dim	min	max
A	1.40	1.60
B	0.40	0.56
B1	0.35	0.48
C	0.35	0.44
D	4.40	4.60
D1	1.35	1.83
e	1.50 BSC	
e1	3.00 BSC	
E	2.29	2.60
H	3.75	4.25
L	0.80	1.20