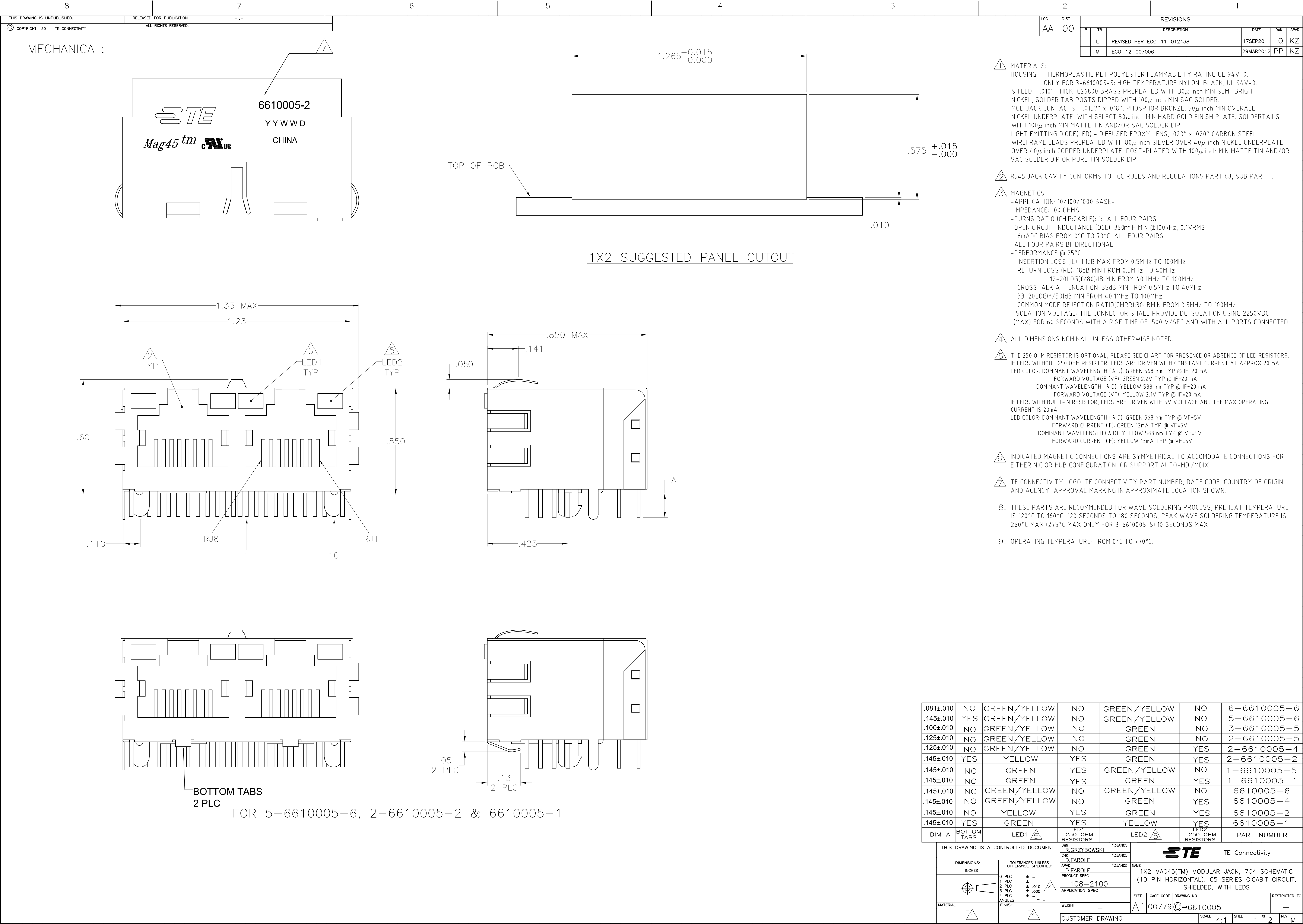
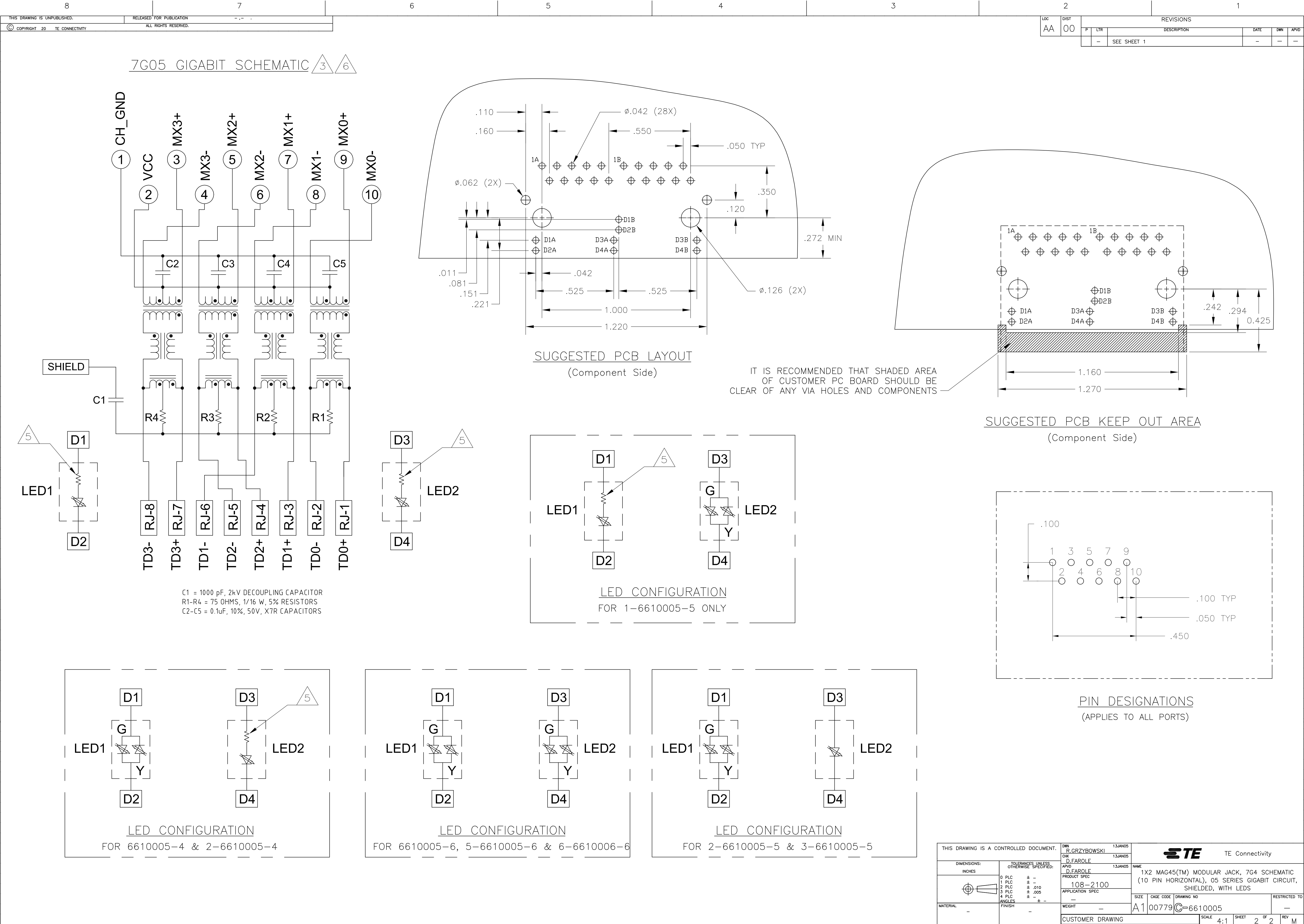






7G05 GIGABIT SCHEMATIC

3

6

1

CH_GND

2

VCC

3

MX3+

4

MX3-

5

MX2+

6

MX2-

7

MX1+

8

MX1-

9

MX0+

10

MX0-

C2

C3

C4

C5

R4

R3

R2

R1

TD3-

TD3+

TD1-

TD2-

TD2+

TD1+

TD0-

TD0+

RJ-8

RJ-7

RJ-6

RJ-5

RJ-4

RJ-3

RJ-2

RJ-1

SHIELD

C1

LED1

D1

D2

C1 = 1000 pF, 2kV DECOUPLING CAPACITOR
R1-R4 = 75 OHMS, 1/16 W, 5% RESISTORS
C2-C5 = 0.1uF, 10%, 50V, X7R CAPACITORS

1A

1B

D1A

D1B

D2A

D2B

D3A

D3B

D4A

D4B

.110

.160

.042

.550

.050 TYP

.350

.120

.272 MIN

.062 (2X)

.011

.081

.151

.221

.525

1.000

1.220

.126 (2X)

SUGGESTED PCB LAYOUT

(Component Side)

1A

1B

D1A

D1B

D2A

D2B

D3A

D3B

D4A

D4B

.242

.294

0.425

1.160

1.270

SUGGESTED PCB KEEP OUT AREA

(Component Side)

LED1

D1

D2

LED2

D3

D4

LED CONFIGURATION

FOR 1-6610005-5 ONLY

LED1

D1

D2

LED2

D3

D4

LED CONFIGURATION

FOR 6610005-4 & 2-6610005-4

LED1

D1

D2

LED2

D3

D4

LED CONFIGURATION

FOR 6610005-6, 5-6610005-6 & 6-6610006-6

LED1

D1

D2

LED2

D3

D4

LED CONFIGURATION

FOR 2-6610005-5 & 3-6610005-5

1

2

3

4

5

6

7

8

9

10

.100

.100 TYP

.050 TYP

.450

PIN DESIGNATIONS

(APPLIES TO ALL PORTS)

THIS DRAWING IS A CONTROLLED DOCUMENT.

DWN

R.GRZYBOWSKI

13JAN05

CHK

D.FAROLE

13JAN05

APVD

D.FAROLE

13JAN05

PRODUCT SPEC

108-2100

APPLICATION SPEC

SIZE

CAGE CODE

DRAWING NO

RESTRICTED TO

A1

00779

C=6610005

CUSTOMER DRAWING

SCALE

4:1

SHEET

2

OF

2

REV

M

DIMENSIONS:

INCHES

0 PLC

±

-

1 PLC

±

-

2 PLC

±

.010

3 PLC

±

.005

4 PLC

±

-

ANGLES

±

-

FINISH

-

4805 (3/11)