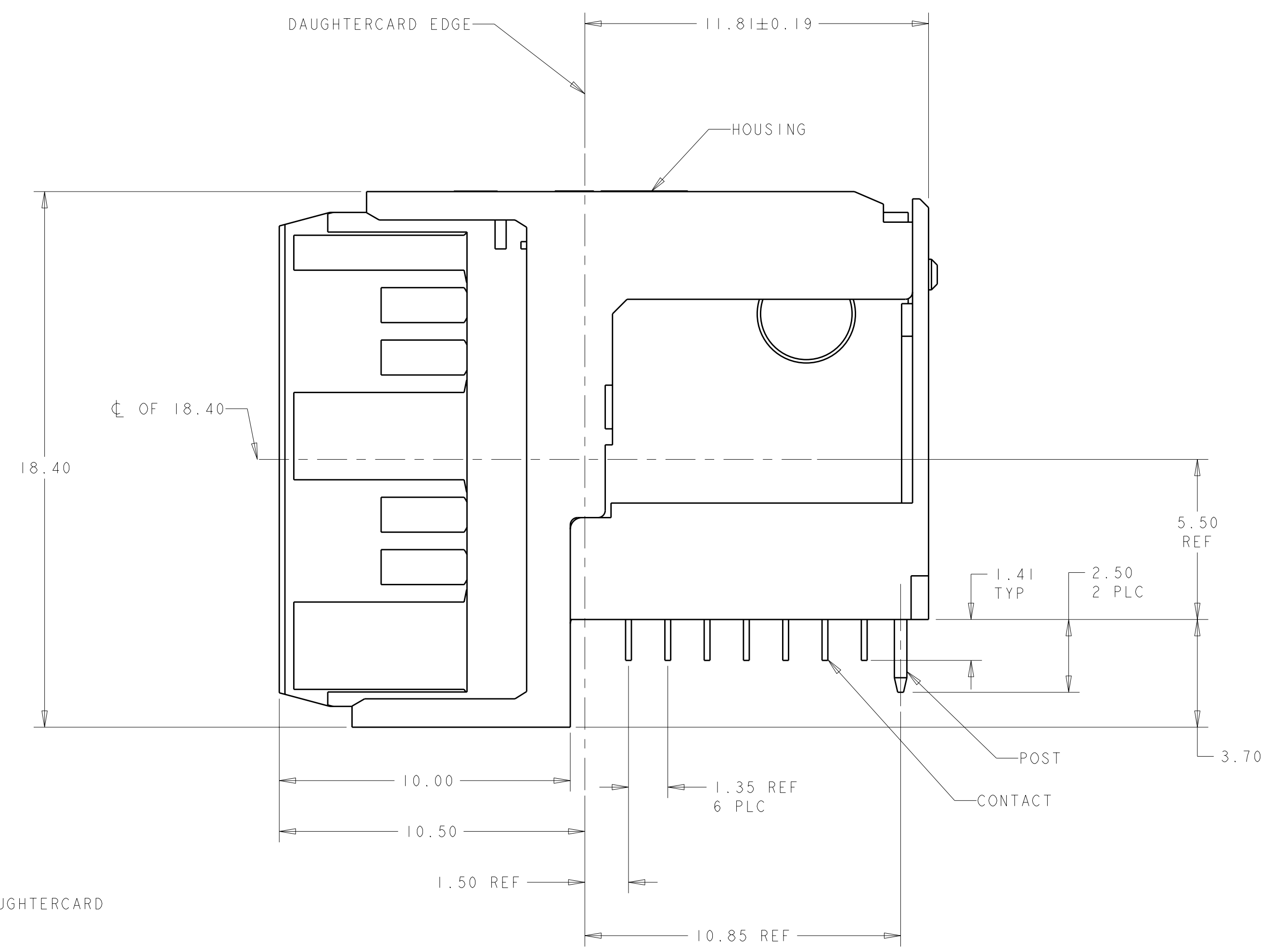
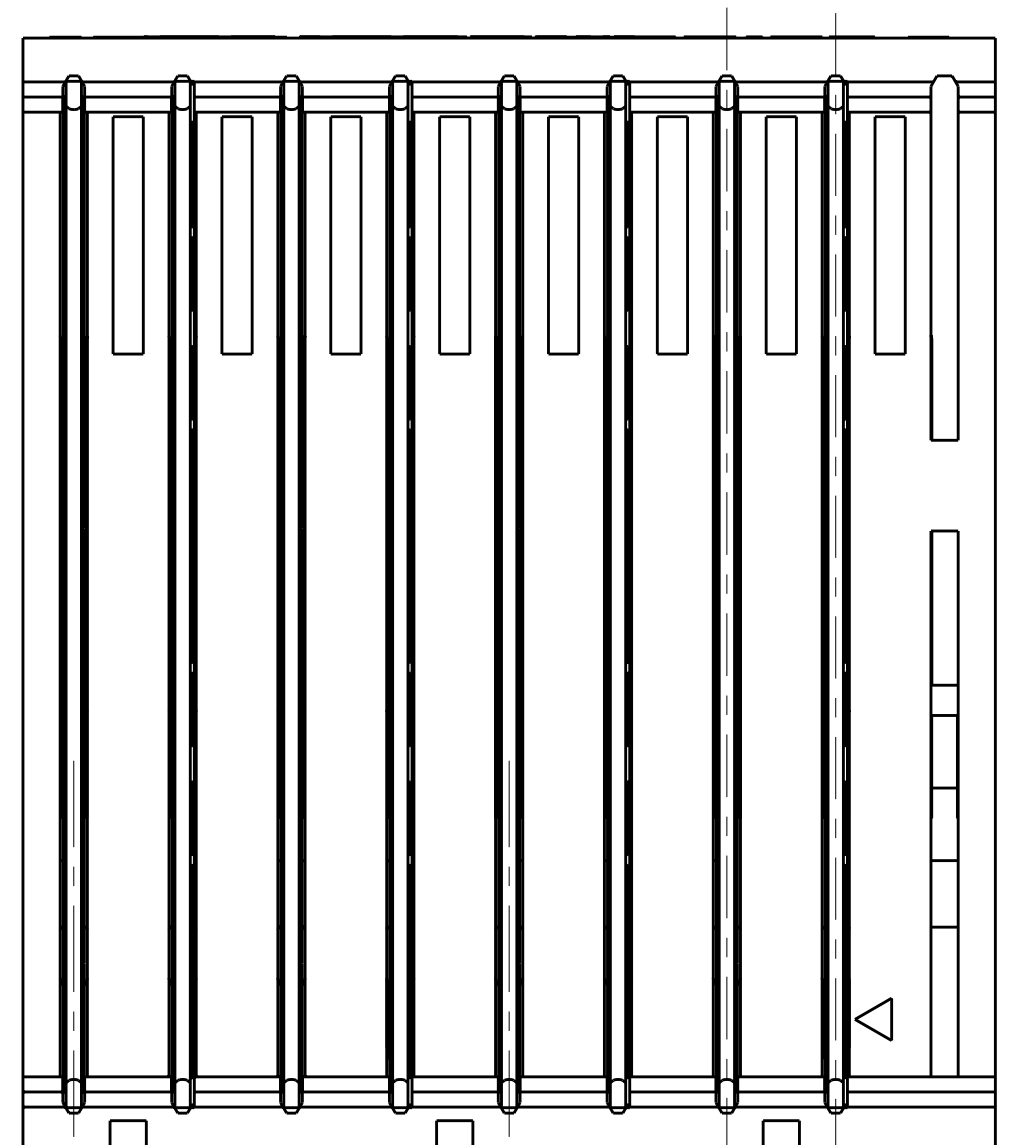


LOC	DIST	REV	DATE	BY	CHK	APPD
GP	00	B1	18MAR2011	RK	HMR	



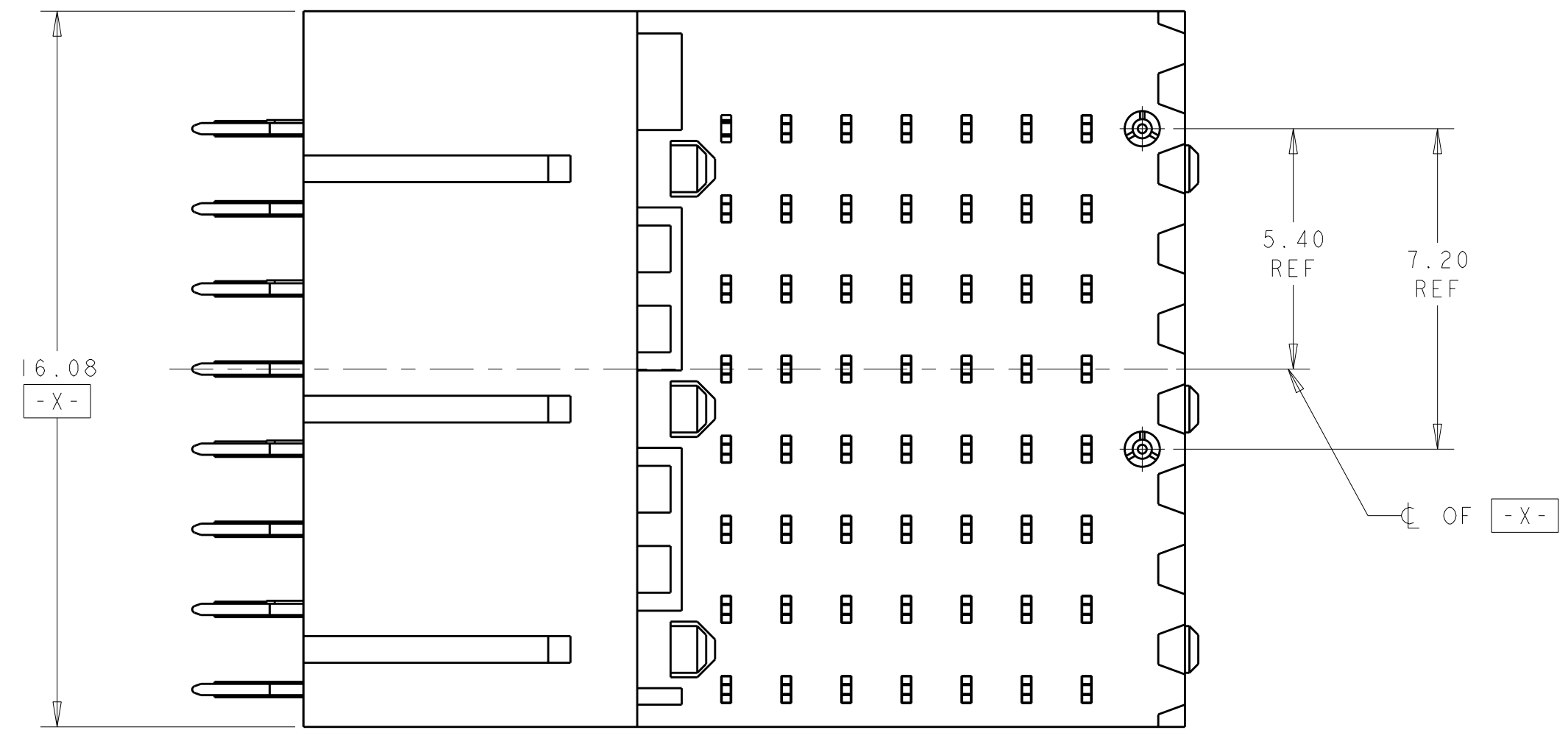
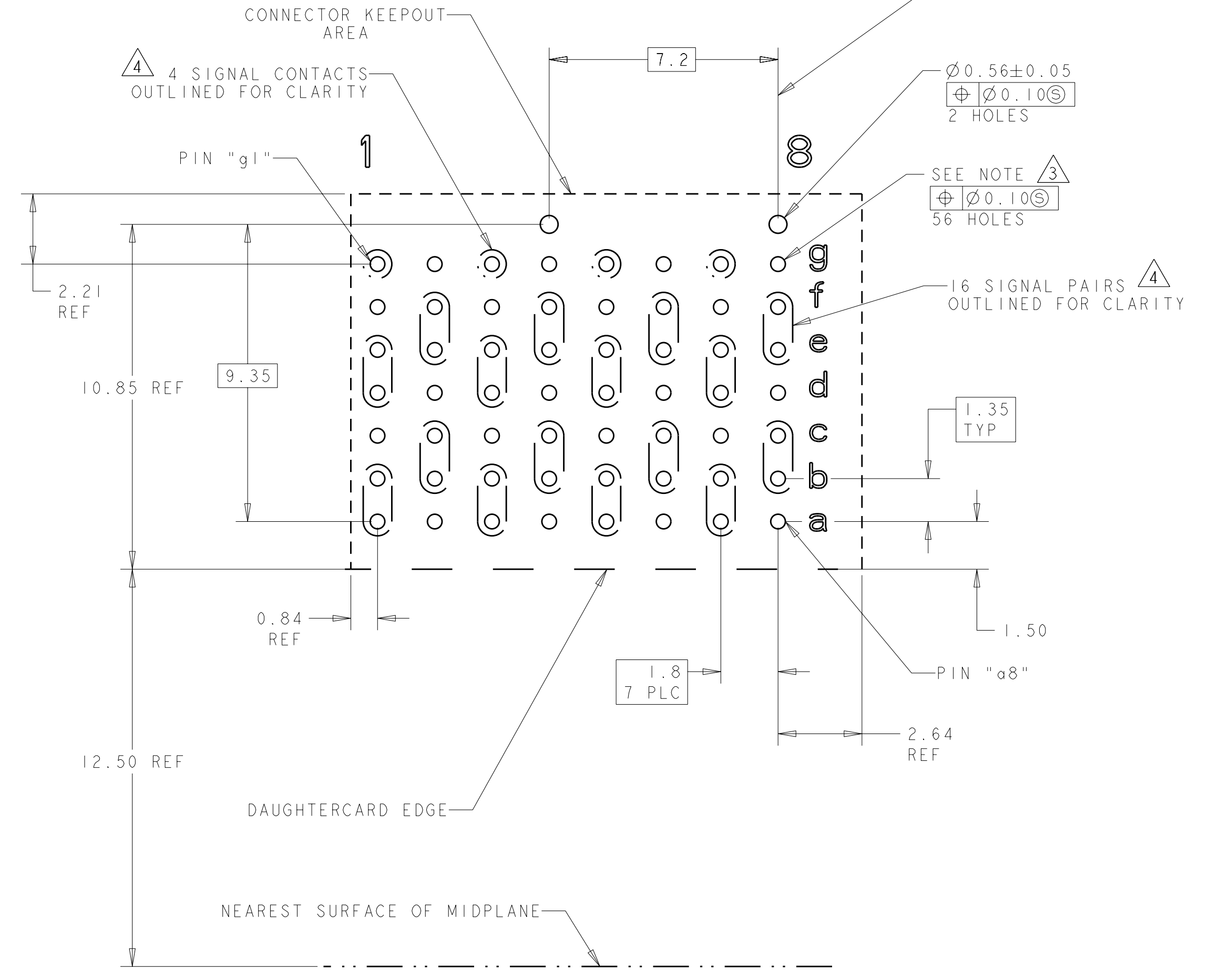
- △ HOUSING: LCP, UL94V0, COLOR: BLACK.
CONTACT: PHOSPHOR BRONZE.
POST: BRASS WIRE
- △ CONTACT: 1.27µm MIN GOLD IN PAD CONTACT AREA, 1.27µm MIN TIN-LEAD ON PCB TAIL OVER 1.27µm MIN. NICKEL OVER ALL.
POST: 1.27µm MIN NICKEL PLATED.
- △ MANUFACTURING TOLERANCE FOR Ø0.46± 0.05 DIAMETER FINISHED HOLE WITH SnPB PLATING:
DRILLED HOLE = Ø0.55±0.02
COPPER PLATING = 0.025-0.050
Sn PB PLATING = 0.0038-0.0124
OR MANUFACTURING TOLERANCE FOR Ø0.475± 0.05 DIAMETER FINISHED HOLE WITHOUT SnPB PLATING:
DRILLED HOLE = Ø0.55±0.02
COPPER PLATING = 0.025-0.050
- △ SEE TABLE 1 FOR INTERCONNECTIONS TO BACKPLANE CONNECTOR.

TABLE 1
INTERCONNECTIONS WITH BACKPLANE CONNECTOR 1410186

TYPICAL INTERCONNECTIONS FOR ODD COLUMN (WAFER): 1,3,5,7		
CONTACT USAGE	DAUGHTERCARD CONNECTOR PIN	BACKPLANE CONNECTOR PIN
SIGNAL PAIR	ax	ax
SIGNAL PAIR	bx	bx
SIGNAL PAIR	dx	ex
SIGNAL PAIR	ex	fx
SIGNAL	gx	ix
GROUND	cx, fx (ALL COMMONED)	cx, dx, gx, hx

TYPICAL INTERCONNECTIONS FOR EVEN COLUMN (WAFER): 2,4,6,8		
CONTACT USAGE	DAUGHTERCARD CONNECTOR PIN	BACKPLANE CONNECTOR PIN
SIGNAL PAIR	bx	cx
SIGNAL PAIR	cx	dx
SIGNAL PAIR	ex	gx
SIGNAL PAIR	fx	hx
GROUND	ax, dx, gx, (ALL COMMONED)	ax, bx, ex, fx, ix

NOTE: "x" DESIGNATES THE COLUMN NUMBER



PC BOARD REAR TRANSITION MODULE (CONNECTOR SIDE)
SCALE 8:1

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN: R. TREA 21FEB2006	1410971-3
DIMENSIONS: mm		CHK: G. GRIFFITH 21FEB2006	PLATING PART NO
TOLERANCES UNLESS OTHERWISE SPECIFIED:		APVD: 21FEB2006	TE Connectivity
Ø	9 PLC ±0.5	PRODUCT SPEC	NAME: RIGHT-ANGLE PLUG ASSEMBLY, 7 ROW, HALF LEFT END, 20.3mm, MultiGig RT2, DAUGHTERCARD CONNECTOR, VITA 46
□	3 PLC ±0.13	APPLICATION SPEC	SIZE: 114-13056
∠	4 PLC ±0.13	FINISH	WEIGHT: -
∠	4 PLC ±0.13	FINISH	RESTRICTED TO
∠	4 PLC ±0.13	FINISH	SCALE: 8:1
∠	4 PLC ±0.13	FINISH	SHEET 1 OF 1
∠	4 PLC ±0.13	FINISH	REV: B1
∠	4 PLC ±0.13	FINISH	CUSTOMER DRAWING