

LOC	DIST	REV	DATE	BY	CHK	APPV
GP	00	B2	12MAR2011	RK	HMR	

- 1 HOUSING: LCP, UL94V0, COLOR: BLACK.  
CONTACT: PHOSPHOR BRONZE.  
POST: BRASS WIRE
- 2 CONTACT: 1.27µm MIN GOLD IN PAD CONTACT AREA  
1.27µm MIN TIN-LEAD ON PCB TAIL  
OVER 1.27µm MIN NICKEL OVER ALL.  
POST 1.27µm MIN NICKEL PLATED.
- 3 MANUFACTURING TOLERANCE FOR Ø0.46±0.05  
DIAMETER FINISHED HOLE WITH Sn /PB PLATING:  
DRILLED HOLE = Ø0.55±0.02  
COPPER PLATING = 0.025-0.050  
Sn /PB PLATING = 0.0038-0.0124  
OR  
MANUFACTURING TOLERANCE FOR Ø0.475±0.05  
DIAMETER FINISHED HOLE WITHOUT Sn /PB PLATING:  
DRILLED HOLE = Ø0.55±0.02  
COPPER PLATING = 0.025-0.050
- 4 SEE TABLE 1 FOR INTERCONNECTIONS TO BACKPLANE CONNECTOR.

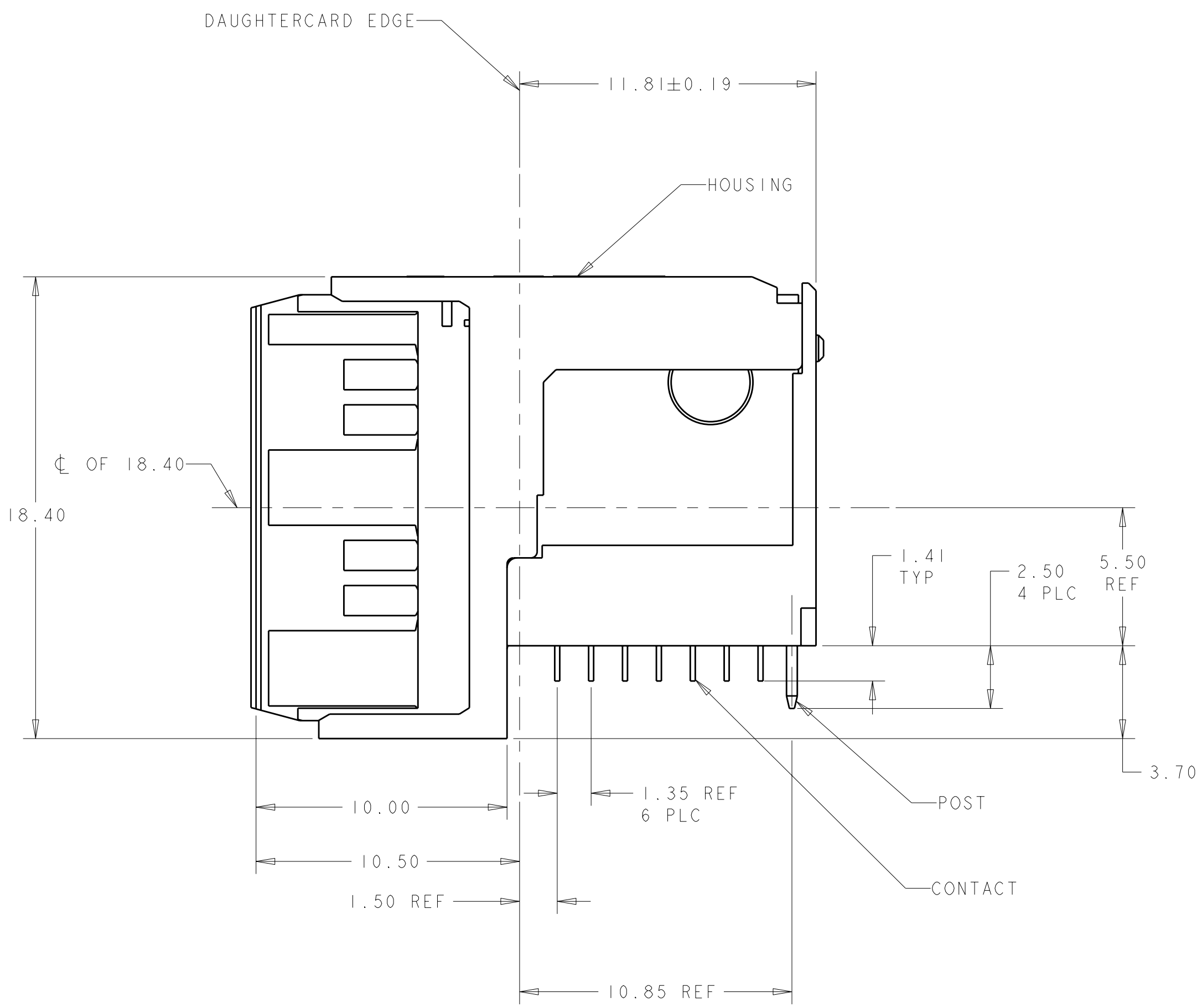
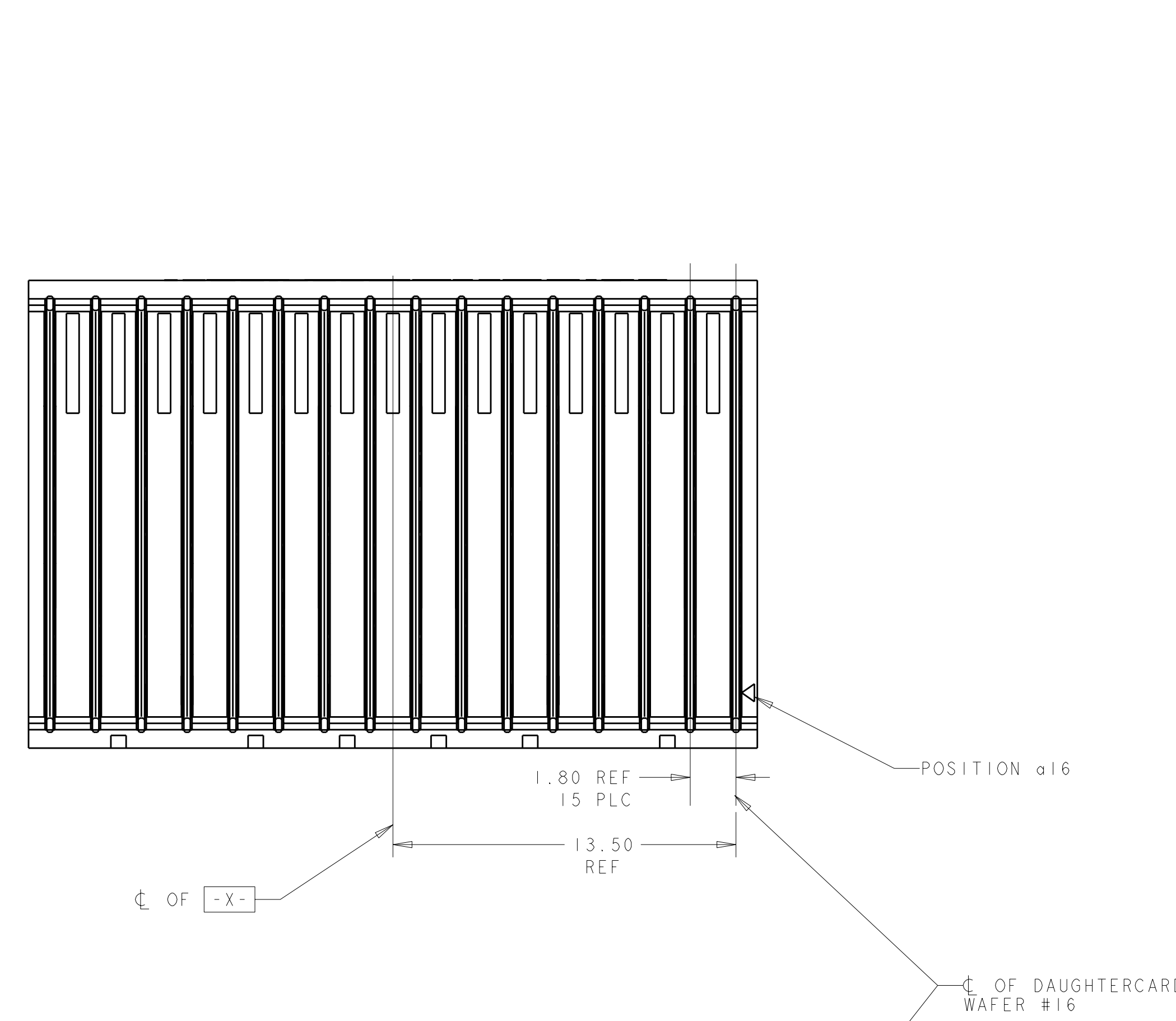
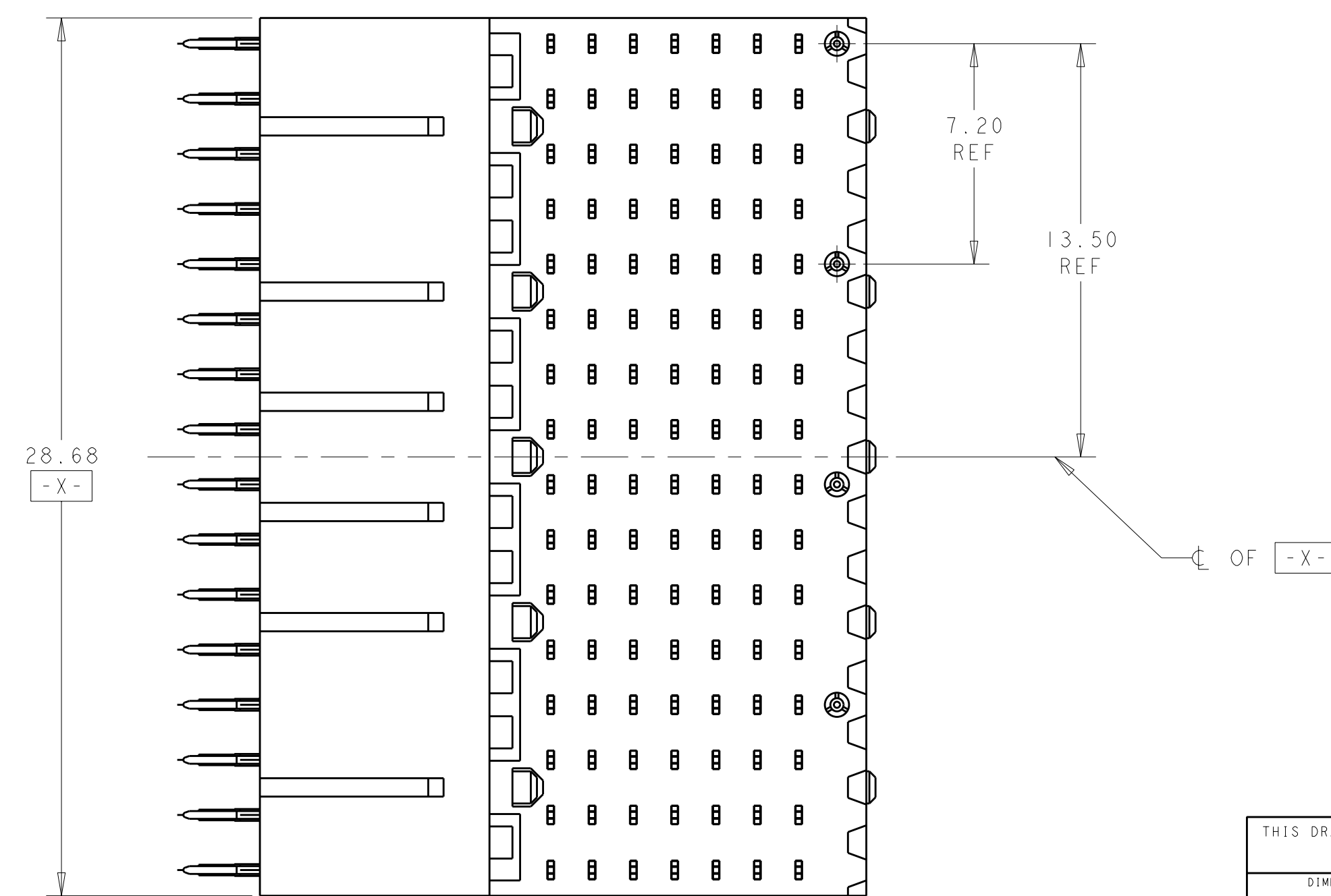
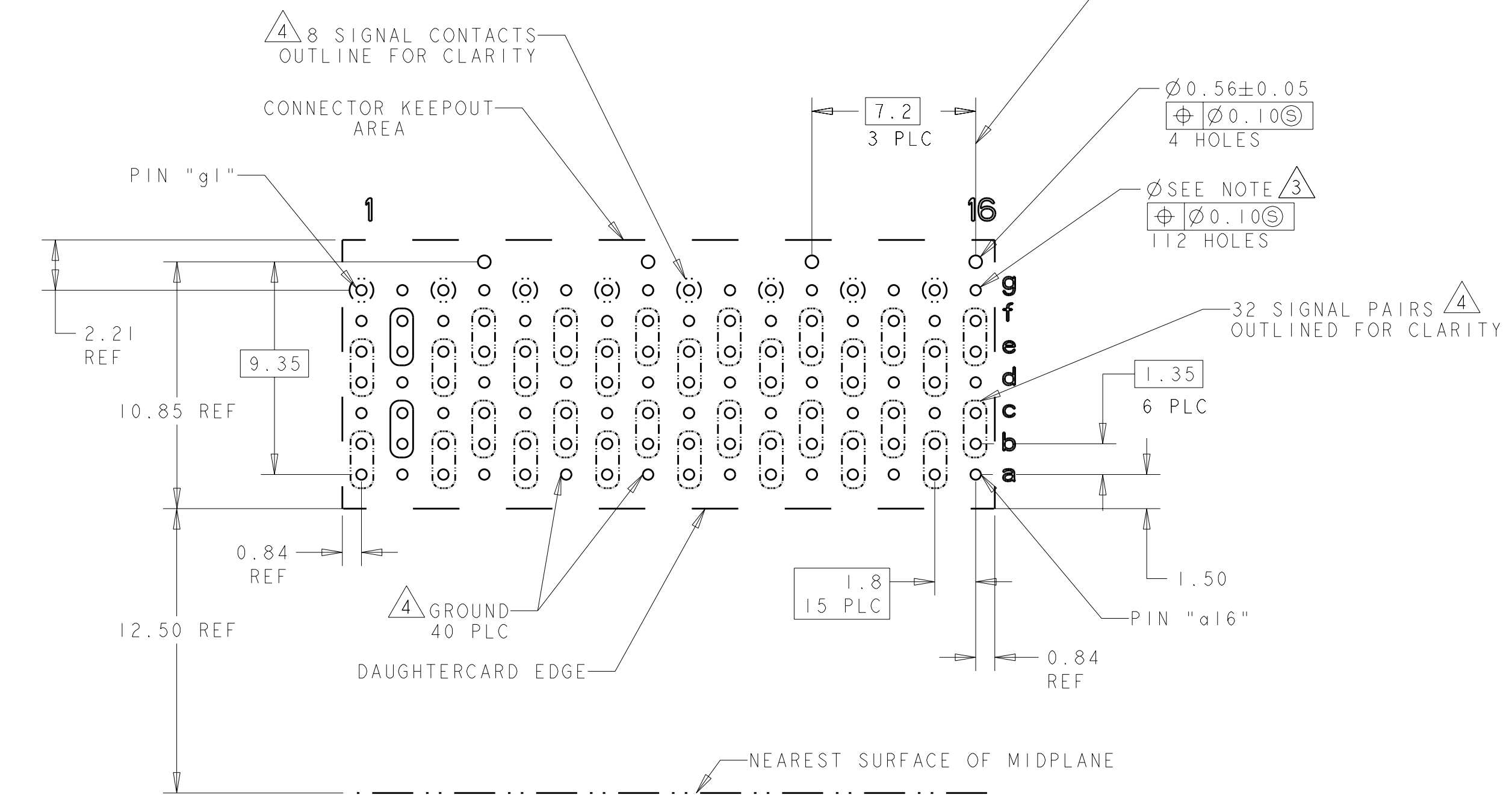


TABLE 1  
 INTERCONNECTIONS WITH BACKPLANE CONNECTOR  
 1410140-1, 1410142-1, 1410966-1, 1410967-1

TYPICAL INTERCONNECTIONS FOR EACH ODD-NUMBERED COLUMN (WAFER): 1, 3, 5, 7, 9, 11, 13, 15		
CONTACT USAGE	DAUGHTERCARD CONNECTOR PIN	BACKPLANE CONNECTOR PIN
SIGNAL PAIR	ax	ax
	bx	bx
SIGNAL PAIR	dx	ex
	ex	fx
SIGNAL	gx	ix
GROUND	cx, fx (ALL COMMONED)	cx, dx, gx, hx

TYPICAL INTERCONNECTIONS FOR EACH EVEN-NUMBERED COLUMN (WAFER): 2, 4, 6, 8, 10, 12, 14, 16		
CONTACT USAGE	DAUGHTERCARD CONNECTOR PIN	BACKPLANE CONNECTOR PIN
SIGNAL PAIR	bx	cx
SIGNAL PAIR	cx	dx
	ex	gx
SIGNAL PAIR	fx	hx
GROUND	ax, dx, gx, (ALL COMMONED)	ax, bx, ex, fx, ix

NOTE: "x" DESIGNATES THE COLUMN NUMBER



PC BOARD REAR TRANSITION MODULE  
 (CONNECTOR SIDE)  
 SCALE 5:1

THIS DRAWING IS A CONTROLLED DOCUMENT.

DIMENSIONS:	TOLERANCES UNLESS OTHERWISE SPECIFIED:	OWN: TREA 22FEB2006	1410975-3
mm	0 PLC ±	CHK: G. GRIFFITH 22FEB2006	PLATING PART NO
	1 PLC ±0.5	APPV: 22FEB2006	1410975-3
	5 PLC ±0.13	PRODUCT SPEC: 108-2072	RIGHT-ANGLE PLUG ASSEMBLY, 7 ROW, CENTER, 20.3mm, MultiGig RT2, DAUGHTERCARD CONNECTOR, DIFFERENTIAL
	4 PLC ±	APPLICATION SPEC: 114-13056	SIZE: 114-13056
	ANGLES ±1°	WEIGHT: -	SCALE: 6:1
MATERIAL:	FINISH:	CUSTOMER DRAWING	REV: B2

STC TE Connectivity