Wide Input, Isolated Eighth-Brick DC/DC Converters



Typical unit

muRata

FEATURES

- Industry-standard through-hole eighth-brick package with 0.9" x 2.3" x 0.36" outline dimensions
- Choice of two wide input ranges, 9-36 Vdc or 18-75 Vdc
- Fixed output from 3.3 to 24 Volts DC up to 75 Watts
- Synchronous rectification yields very high efficiency and low power dissipation
- Operating temperature range from -40 to +85°C. with derating
- Up to 1500 Volt DC isolation (basic insulation)
- Outstanding thermal performance and derating
- Extensive self-protection, overtemperature and overload features with no output reverse conduction
- On/Off control, trim and remote sense functions
- Designed to meet UL/EN/IEC 60950-1, CAN/ CSA-C22.2 No. 60950-1 safety approvals and FCC RFI/EMI certification
- Pre-bias operation for startup protection

The UWE Series "Eighth-Brick" DC/DC Converters are high-current isolated power modules designed for use in high-density system boards.

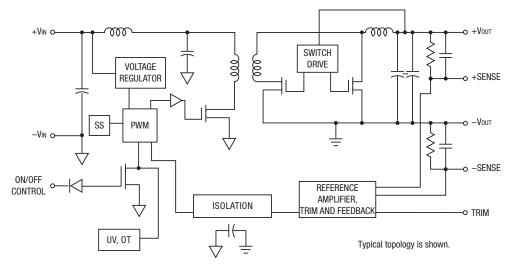
PRODUCT OVERVIEW

With dimensions of only 0.9 by 2.3 by 0.36 inches, the UWE series open frame DC/DC converters deliver up to 75 Watts in an industry-standard "eighth-brick" through-hole package. This format can plug directly into quarter-brick pinouts. Several standard fixed-output voltages from 3.3 Vdc to 24 Vdc assure compatibility in embedded equipment, CPU cards and instrument subsystems. The extended 4-to-1 input power range (9-36V) is ideal for battery-powered, telecom or portable applications. Very high efficiency means no fans or temperature deratings in many applications. An optional thermal mounting baseplate extends operation into most conceivable environments.

The synchronous rectifier design uses the maximum available duty cycle for greatest efficiency and low power dissipation with no reverse output conduction. Other features include low on-resistance FET's, planar magnetics and heavycopper PC boards. These deliver low output noise, tight line/load regulation, stable no-load operation and fast load step response. All units are precision assembled in a highly automated facility with ISOtraceable manufacturing quality standards.

Isolation of 1500 Volts assures safety and fully differential (floating) operation for greatest application flexibility. On-board Sense inputs compensate for line drop errors at high output currents. Outputs are trimmable within $\pm 10\%$ of nominal voltage. The UWE series are functionally complete.

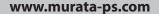
A wealth of protection features prevents damage to both the converter and outside circuits. Inputs are protected from undervoltage and outputs feature short circuit protection, overcurrent and excess temperature shut down. Overloads automatically recover using the "hiccup" technique upon fault removal. The UWE is designed to meet all standard safety and EMI/RFI certifications. All units meet RoHS-6 (no lead) hazardous materials compliance.



Simplified Block Diagram



D



UWE Series

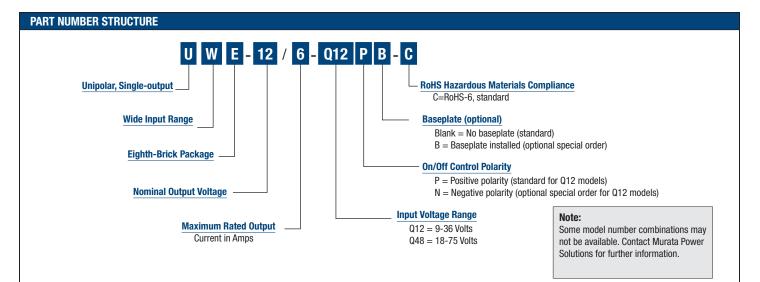
Wide Input, Isolated Eighth-Brick DC/DC Converters

| SPECIFICATION SUMMA | ARY AND (| ORDERIN | G GUIDE (| D | | | | | | | | | | |
|---------------------|-----------|---------|-----------|--------|--|----------|----------------|-------------|---------------|-----------------|-------------------|-------|-------|----------------|
| | | | | Output | | | Input | | | | Efficiency | | | |
| | Vout | Іоит | Power | R/N (m | Vp-p) ③ | Regulati | on (max.) | VIN Nom. | Range | lın, no load | lın, full load | | | Package |
| Root Model ① | (V) | (A) | (W) | Тур. | Max. | Line | Load | (V) | (V) | (mA) | (A) | Min. | Тур. | (Case, Pinout) |
| UWE-3.3/20-Q12P-C | 3.3 | 20 | 66 | 80 | 125 | ±0.25% | ±0.25% | 12 | 9-36 | 160 | 6.18 | 87% | 89% | |
| UWE-3.3/20-Q48N-C | 3.3 | 20 | 66 | | Please contact Murata Power Solutions for further information. | | | | | | | | | |
| UWE-5/15-Q12P-C | 5.0 | 15 | 75 | 80 | 125 | ±0.25% | ±0.125% | 12 | 9-36 | 185 | 6.87 | 89% | 91% | |
| UWE-5/15-Q48N-C | 5.0 | 15 | 75 | | | Please | contact Murata | a Power Sol | utions for fi | urther infor | mation. | | | C77, P32 |
| UWE-12/6-Q12P-C | 12.0 | 6 | 72 | 120 | 180 | ±0.125% | ±0.05% | 12 | 9-36 | 75 | 6.56 | 90% | 91.5% | |
| UWE-12/6-Q48N-C | 12.0 | 6 | 72 | 115 | 150 | ±0.1% | ±0.075% | 48 | 18-75 | 90 | 1.65 | 89% | 91% | |
| UWE-24/3-Q12P-C | 24.0 | 3 | 72 | 190 | 275 | ±0.125% | ±0.125% | 12 | 9-36 | 110 | 6.70 | 88.3% | 89.5% | |

① Please refer to the part number structure for additonal ordering model numbers and options.
② All specifications are at nominal line voltage, nominal output voltage and full load, +25° C. unless otherwise noted. See detailed specifications.

③ Output capacitors are 1 µF ceramic in parallel with 10 µF electrolytic. Input cap is 100 µF. All caps are low ESR types. Contact Murata Power Solutions for details.

I/O caps are necessary for our test equipment and may not be needed for your application.
Load range: 0.1-3A. This is required only for our test equipment. The converter will operate at zero output current.



DETAIL SPECIFICATIONS, Q12 MODELS

| | UWE-3.3/20-Q12 | UWE-5/15-Q12 | UWE-12/6-012 | UWE-24/3-Q12 ® |
|---|----------------|--------------|--------------|----------------|
| | 0WL-3.3/20-Q12 | 0WL-3/13-Q12 | 00012/0-012 | 0112-24/3-012 |
| Specs are typical unless noted. | | | | |
| INPUT | | | | |
| Input voltage range | | See order | ring guide | |
| Start-up threshold, Volts | 9.5 | | 9 | |
| Undervoltage shutdown, V. | 8.5 | | 8 | |
| Overvoltage shutdown, V. | | no | ne | |
| Reflected (back) ripple current, mA pk-pk | 25 | 10 | 40 | 40 |
| Suggested external fuse, A | 25 | 20 | 20 | 20 |
| Input current | | | | |
| Full load conditions | | See order | ring guide | |
| Inrush transient, A ² sec | | 0.1 / | A²sec | |
| Input current if output is in short circuit, mA | 250 | 200 | 250 | 250 |
| No load, mA | 160 | 185 | 75 | 110 |
| Low line (Vin=min.), Amps | 8.33 | 9.42 | 8.89 | 9.04 |
| Standby mode, mA | 8 | 5 | 5 | 5 |

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| DETAIL SPECIFICATIONS, Q12 MODELS, CONTIUED | | | | | |
|---|--------------------|-------------------------------|-----------------------------|--------------|--|
| INPUT (continued) | UWE-3.3/20-Q12 | UWE-5/15-Q12 | UWE-12/6-Q12 | UWE-24/3-Q12 | |
| (Off, UV, OT shutdown) Internal input filter type | | Ŀ | -C | | |
| Reverse polarity protection | | Ye | es | | |
| Remote On/Off control | | | | | |
| Positive logic ("P" model suffix) | | OFF=Ground pi | n to +0.8V max. | | |
| | | ON=open pin or - | +3.5 to +Vin max. | | |
| Negative logic ("N" model suffix) | | OFF=open pin or | +5V to +Vin max. | | |
| | | ON=Ground pin o | r 0 to +0.8V max. | | |
| Current, mA | | - | 1 | | |
| OUTPUT | | | | | |
| Voltage output range | | See order | ring guide | | |
| Voltage output accuracy | | ±1% of Vnom | n., (50% load) | | |
| Adjustment range | | -10 to +10 | % of Vnom. | | |
| Temperature coefficient | | ±0.02% of Vol | ut range per °C | | |
| Minimum loading | | No minin | num load | | |
| Remote sense compensation | | +10% | max. | | |
| Ripple/noise (20 MHz bandwidth) | | See order | ring guide | | |
| Line/Load regulation | See ordering guide | | | | |
| Efficiency | See ordering guide | | | | |
| Maximum capacitive loading, μF low ESR <0.02Ω max., resistive load | 10,000 | 10,000 | 4,700 | 1500 | |
| Isolation voltage | 1 | | 1 | | |
| Input to Output, Volts min. DC | | 15 | 00 | | |
| Input to baseplate, Volts min. DC | 1500 | | | | |
| Baseplate to output, Volts min. DC | 750 | | | | |
| Isolation resistance, $M\Omega$ | | 1(| 00 | | |
| Isolation capacitance, pF | 1500 | 1000 | 1000 | 1000 | |
| Isolation safety rating | | Basic in | sulation | | |
| Current limit inception (98% of Vout, after warmup), Amps | 27 | 22.5 | 8.5 | 4.0 | |
| Short circuit protection method | Curre | nt limiting, hiccup autoresta | rt. Remove overload for rec | overy. | |
| Short circuit current, Amps | 0.5 | 1.0 | 1.5 | 1.0 | |
| Short circuit duration | | Continuous, output shorte | ed to ground. No damage. | | |
| Overvoltage protection, Volts (via magnetic feedback) | 4.5 | 6 | 15 | 29 | |
| DYNAMIC CHARACTERISTICS | | 1 | | | |
| Dynamic load response, µSec (50-75-50% load step) to ±1% of final value | 50 | | | | |
| Start-up time | 1 | | | | |
| Vin to Vout regulated, mSec | 20 | 20 | 40 | 40 | |
| Remote On/Off to Vout regulated, mSec | 5 | 15 | 30 | 30 | |
| Switching frequency, KHz | 245±25 | 215-250 | 275 ±25 | 215 ±15 | |



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| DETAIL SPECIFICATIONS, Q12 MODELS, CONTIUED | | | | | | |
|---|--|---|--------------------------|----------------------|--|--|
| ENVIRONMENTAL | UWE-3.3/20-Q12 | UWE-5/15-Q12 | UWE-12/6-Q12 | UWE-24/3-Q12 | | |
| Calculated MTBF (Telcordia SR-332) | | 3,755,203 hours (UWE-12/6-Q12N-C) 1,248,001 hours (UWE-3.3/20-Q12) | | | | |
| Calculated MTBF (MIL-HDBK-217N2) | | 1,239,521 hours (UWE-12/6-Q12N-C) 1,089,141 hours (UWE-3.3/20-Q12) | | | | |
| Operating temperature range with derating, °C (see Derating curves) | | -40 to +85 with derating | | | | |
| Storage temperature range, °C | | -55 to | +125 | | | |
| Thermal protection/shutdown, °C | | +120 | | | | |
| Relative humidity | to +85°C/85% non-condensing | | | | | |
| PHYSICAL | | | | | | |
| Outline dimensions | See mechanical specs | | | | | |
| Pin material | Copper alloy | | | | | |
| Pin Finish | Nickel underplate with gold overplate (see mechanical specs for details) | | | | | |
| Pin diameter, inches | | 0.04/ | 0.062 | | | |
| Pin diameter, mm | | 1.016 | /1.575 | | | |
| Weight, ounces | 0.7 | | | | | |
| Weight, grams | | 2 | 0 | | | |
| Electromagnetic interference (conducted) | Designed to meet F | CC part 15, class B, EN550 | 22 conducted and CISPR22 | 2 class B conducted. | | |
| Safety | Designed to | meet UL/cUL 60950-1, CS | A-C22.2 No.60950-1, IEC/ | EN 60950-1 | | |

| DETAIL SPECIFICATIONS, Q48 MODELS | | |
|--|----------------------------------|--|
| | UWE-12/6-Q48 | |
| Specs are typical unless noted. | | |
| INPUT | | |
| Input voltage range | See ordering guide | |
| Start-up threshold, Volts | 17.5 | |
| Undervoltage shutdown, V. | 16 | |
| Overvoltage shutdown, V. | none | |
| Reflected (back) ripple current, mA pk-pk | 40 | |
| Suggested external fuse, A | 10 | |
| Input current | | |
| Full load conditions | See ordering guide | |
| Inrush transient, A ² sec | 0.1 A ² sec | |
| Input current if output is in short circuit, mA | 100 | |
| No load, mA | 90 | |
| Low line (Vin=min.), Amps | 4.35 | |
| Standby mode, mA | 4 | |
| (Off, UV, OT shutdown) Internal input filter type | Pi-type | |
| Reverse polarity protection | None | |
| Remote On/Off control | · | |
| Positive logic (IIDII model suffix) | OFF=Ground pin to +0.8V max. | |
| Positive logic ("P" model suffix) | ON=open pin or +3.5 to +Vin max. | |
| Negative Logic (INIII model cuffin) | OFF=open pin or +5V to +Vin max. | |
| Negative logic ("N" model suffix) | ON=Ground pin or 0 to +0.8V max. | |
| Current, mA | 1 | |



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| DETAIL SPECIFICATIONS, Q48 MODELS, CONTIUED | | |
|---|---|--|
| OUTPUT | UWE-12/6-Q48 | |
| Voltage output range | See ordering guide | |
| Voltage output accuracy | ±1% of Vnom., (50% load) | |
| Adjustment range | -10 to +10% of Vnom. | |
| Temperature coefficient | ±0.02% of Vout range per °C | |
| Minimum loading | No minimum load | |
| Remote sense compensation | +10% max. | |
| Ripple/noise (20 MHz bandwidth) | See ordering guide | |
| Line/Load regulation | See ordering guide | |
| Efficiency | See ordering guide | |
| Maximum capacitive loading, μF low ESR <0.02 Ω max., resistive load | 4,700 | |
| Isolation voltage | | |
| Input to Output, Volts min. DC | 2250 | |
| Input to baseplate, Volts min. DC | 1500 | |
| Baseplate to output, Volts min. DC | 750 | |
| Isolation resistance, $M\Omega$ | 100 | |
| Isolation capacitance, pF | 1000 | |
| Isolation safety rating | Basic insulation | |
| Current limit inception (98% of Vout, after warmup), Amps | 8.0 | |
| Short circuit protection method | Current limiting, hiccup autorestart. Remove overload for recovery. | |
| Short circuit current, Amps | 1.0 | |
| Short circuit duration | Continuous, output shorted to ground. No damage. | |
| Overvoltage protection, Volts (via magnetic feedback) | 15 | |
| DYNAMIC CHARACTERISTICS | | |
| Dynamic load response, µSec (50-75-50% load step) to ±1% of final value | 50 | |
| Start-up time | | |
| Vin to Vout regulated, mSec | 30 | |
| Remote On/Off to Vout regulated, mSec | 20 | |
| Switching frequency, KHz | 220±20 | |
| ENVIRONMENTAL | | |
| Calculated MTBF (Telcordia SR-332) | | |
| Calculated MTBF (MIL-HDBK-217N2) | | |
| Operating temperature range with derating, °C (see Derating curves) | -40 to +85 with derating | |
| Storage temperature range, °C | -55 to +125 | |
| Thermal protection/shutdown, °C | +120 | |
| Relative humidity | to +85°C/85% non-condensing | |



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| DETAIL SPECIFICATIONS, Q48 MODELS, CONTIUED | | |
|---|--|--|
| PHYSICAL | UWE-12/6-Q48 | |
| Outline dimensions | See mechanical specs | |
| Pin material | Copper alloy | |
| Pin Finish | Nickel underplate with gold overplate (see mechanical specs for details) | |
| Pin diameter, inches | 0.04/0.062 | |
| Pin diameter, mm | 1.016/1.575 | |
| Weight, ounces | 0.7 | |
| Weight, grams | 20 | |
| Electromagnetic interference (conducted) | Designed to meet FCC part 15, class B, EN55022 conducted and CISPR22 class B conducted. | |
| Safety | Designed to meet UL/cUL 60950-1, CSA-C22.2 No.60950-1, IEC/EN 60950-1 | |

SPECIFICATION NOTES

CAUTION: This product is not internally fused. To comply with safety agency certifications and to avoid injury to personnel or equipment, the user must supply an external fast-blow fuse to the input terminals. See fuse information.

1 All Q12 models are tested and specified with external 1µF and 10µF paralleled ceramic/tantalum output capacitors and a 100µF external input capacitor. Q48 models test with a 35µF input cap. All capacitors are low ESR types. Contact Murata Power Solutions for details. These capacitors are necessary to accommodate our test equipment and may not be required to achieve specified performance in your applications. However, Murata Power Solutions recommends using these capacitors in your application. All models are stable and regulate within spec under no-load conditions.

All specifications are typical unless noted. General conditions for Specifications are +25° C, Vin=nominal, Vout=nominal, full load. Adequate airflow must be supplied for extended testing under power.

- 2 Input Ripple Current is tested and specified over a 5 Hz to 20 MHz bandwidth. Input filtering is Cin=33 μ F, Cbus=220 μ F, Lbus=12 μ H.
- 3 Note that Maximum Power Derating curves indicate an average current at nominal input voltage. At higher temperatures and/or lower airflow, the DC/DC converter will tolerate brief full current outputs if the total RMS current over time does not exceed the Derating curve. All Derating curves are presented at sea level altitude. Be aware that power dissipation degrades as altitude increases.
- 4 Mean Time Before Failure is calculated using the Telcordia (Belcore) SR-332 Issue 1, Case 3, Fits 266, ground fixed conditions, Tambient = $+30^{\circ}$ C, full output load, natural air convection.
- 5 The Remote On/Off Control is normally controlled by a switch or open collector or open drain transistor. But it may also be driven with external logic or by applying appropriate external voltages which are referenced to Input Common.
- 6 Short circuit shutdown begins when the output voltage degrades approximately 2% from the selected setting.
- 7 The outputs are not intended to sink appreciable reverse current.
- 8 Output noise may be further reduced by adding an external filter. See I/O Filtering and Noise Reduction. Larger caps (especially low-ESR ceramic capacitors) may slow transient response or degrade stability. Use only as much output filtering as

needed to achieve your noise requirements and no more. Thoroughly test your system under full load with all components installed.

- 9 All models are fully operational and meet published specifications, including "cold start" at -40° C. At full power, the package temperature of all on-board components must not exceed +128° C.
- 10 Regulation specifications describe the deviation as the line input voltage or output load current is varied from a nominal midpoint value to either extreme.
- 11 If the user adjusts the output voltage, accuracy is dependent on user-supplied trim resistors. To achieve high accuracy, use ±1% or better tolerance metal-film resistors. If no trim is installed, the converter will achieve its rated accuracy. Do not exceed maximum power specifications when adjusting the output trim.
- 12 Output current limit and short circuit protection is non-latching. When the overcurrent fault is removed, the converter will immediately recover.
- 13 Alternate pin length and/or other output voltages may be available under special quantity order.
- 14 At zero output current, the output may contain low frequency components which exceed the ripple specification. The output may be operated indefinitely with no load.
- 15 Input Fusing: To ensure reverse input protection with full output load, always connect an external input fast-blow <u>fuse</u> in series with the +Vin input. Use approximately twice the full input current rating at the selected input voltage.
- 16 "Hiccup" overcurrent operation repeatedly attempts to restart the converter with a brief, full-current output. If the overcurrent condition still exists, the restart current will be removed and then tried again. This short current pulse prevents overheating and damaging the converter. Once the fault is removed, the converter immediately recovers normal operation.
- 17 Note that the converter will operate up to the rated baseplate maximum temperature with the baseplate installed and properly heat sunk. To avoid thermal selfprotection shutdown, do not exceed this maximum baseplate temperature.
- 18 UWE-24/3-Q12 undervoltage shutdown of 8.0V is at half load.
- 19 UWE-24/3-Q12 output overvoltage protection requires 0.3A minimum load.
- 20 Pre-bias operation: Startup will succeed if the output setpoint voltage is higher than the pre-existing external output voltage.

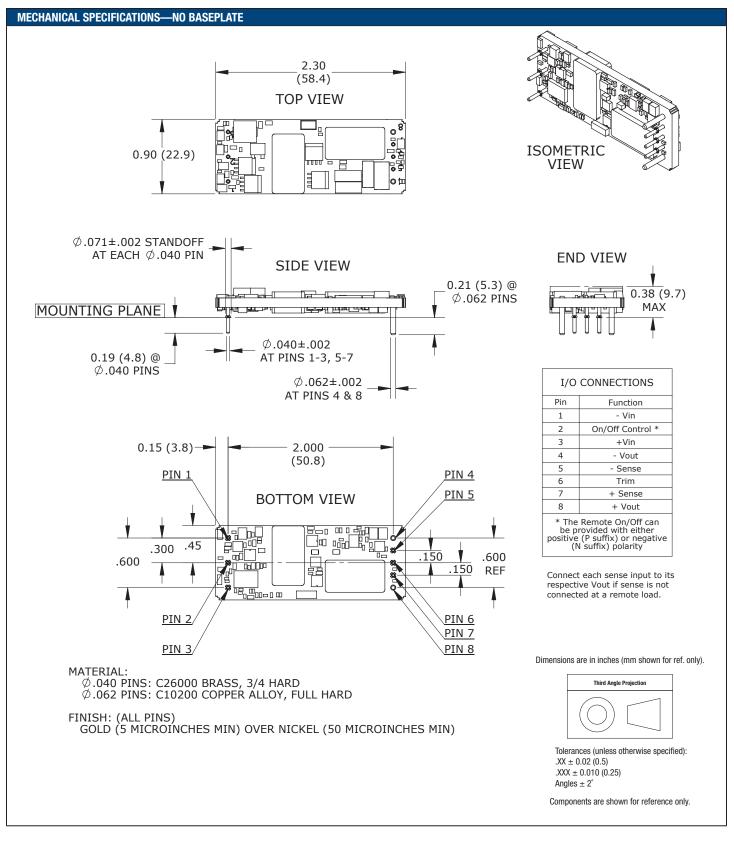




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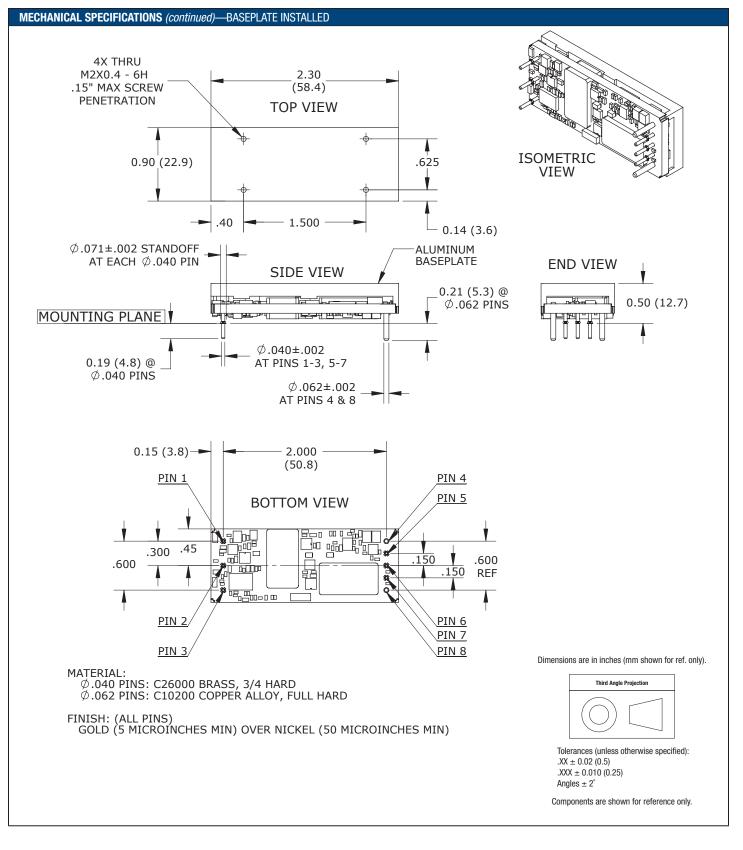
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UWE Series

Soldering Guidelines

Murata Power Solutions recommends the specifications below when installing these converters. These specifications vary depending on the solder type. Exceeding these specifications may cause damage to the product. Be cautious when there is high atmospheric humidity. We strongly recommend a mild pre-bake (100° C. for 30 minutes). Your production environment may differ; therefore please thoroughly review these guidelines with your process engineers.

| Wave Solder Operations for through | -hole mounted products (THMT) |
|------------------------------------|-------------------------------|
| For Sn/Ag/Cu based solders: | |
| Maximum Preheat Temperature | 115° C. |
| Maximum Pot Temperature | 270° C. |
| Maximum Solder Dwell Time | 7 seconds |
| For Sn/Pb based solders: | |
| Maximum Preheat Temperature | 105° C. |
| Maximum Pot Temperature | 250° C. |
| Maximum Solder Dwell Time | 6 seconds |
| | |

Absolute Maximum Ratings

Input Voltage

| 0-36 VDC 0-50 VDC 0-75 VDC 0-100 VDC -0.7 V. min to +Vin max. See Fuse section. |
|--|
| 0-75 VDC 0-100 VDC -0.7 V. min to +Vin max. |
| 0-100 VDC -0.7 V. min to +Vin max. |
| -0.7 V. min to +Vin max. |
| |
| See Fuse section |
| |
| Vout nom. +20% max. |
| Current-limited. Devices can withstand sustained short circuit without damage. |
| Device includes electronic over- temperature shutdown protection under normal operation. |
| -55 to +125° C. |
| See soldering specifications |
| |

Absolute maximums are stress ratings. Exposure of devices to greater than any of these conditions may adversely affect long-term reliability. Proper operation under conditions other than those listed in the Performance/Functional Specifications Table is not implied or recommended.

TECHNICAL NOTES

Input Fusing

Certain applications and/or safety agencies may require the installation of fuses at the inputs of power conversion components. Fuses should also be used if the possibility of sustained, non-current-limited, input-voltage polarity reversals exist. For MPS UWE DC/DC Converters, you should use fast-blow type fuses, installed in the ungrounded input supply line. Refer to the specifications for fuse values.

All relevant national and international safety standards and regulations must be observed by the installer. For system safety agency approvals, the converters must be installed in compliance with the requirements of the end-use safety standard, e.g., IEC/EN/UL60950-1.

Input Undervoltage Shutdown and Start-Up Threshold

Under normal start-up conditions, devices will not begin to regulate until the ramping-up input voltage exceeds the Start-Up Threshold Voltage. Once operating, devices will not turn off until the input voltage drops below the



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Undervoltage Shutdown limit. Subsequent re-start will not occur until the input is brought back up to the Start-Up Threshold. This built in hysteresis prevents any unstable on/off situations from occurring at a single input voltage.

Start-Up Time

The V_{IN} to V_{OUT} Start-Up Time is the interval of time between the point at which the ramping input voltage crosses the Start-Up Threshold and the fully loaded output voltage enters and remains within its specified accuracy band. Actual measured times will vary with input source impedance, external input/output capacitance, and load. The UWE Series implements a soft start circuit that limits the duty cycle of its PWM controller at power up, thereby limiting the input inrush current.

The On/Off Control to Vout start-up time assumes the converter has its nominal input voltage applied but is turned off via the On/Off Control pin. The specification defines the interval between the point at which the converter is turned on and the fully loaded output voltage enters and remains within its specified accuracy band. Similar to the V_{IN} to V_{OUT} start-up, the On/Off Control to V_{OUT} start-up time is also governed by the internal soft start circuitry and external load capacitance.

The difference in start up time from V_{IN} to V_{OUT} and from On/Off Control to V_{OUT} is therefore insignificant.

Input Source Impedance

UWE converters must be driven from a low ac-impedance input source. The DC/DC's performance and stability can be compromised by the use of highly inductive source impedances. For optimum performance, components should be mounted close to the DC/DC converter. If the application has a high source impedance, low V_{IN} models can benefit from increased external input capacitance.

I/O Filtering, Input Ripple Current, and Output Noise

All models in the UWE Converters are tested/specified for input reflected ripple current and output noise using the specified external input/output components/ circuits and layout as shown in the following two figures.

External input capacitors (C_N in Figure 2) serve primarily as energy-storage elements, minimizing line voltage variations caused by transient IR drops in conductors from backplane to the DC/DC. Input caps should be selected for bulk capacitance (at appropriate frequencies), low ESR, and high rms-ripple-current ratings. The switching nature of DC/DC converters requires that dc voltage sources have low ac impedance as highly inductive source impedance can affect system stability. In Figure 2, C_{BUS} and L_{BUS} simulate a typical dc voltage bus. Your specific system configuration may necessitate additional considerations.

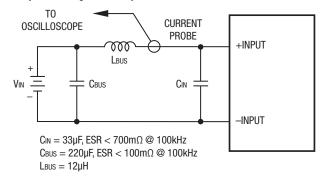


Figure 2. Measuring Input Ripple Current

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In critical applications, output ripple/noise (also referred to as periodic and random deviations or PARD) may be reduced below specified limits using filtering techniques, the simplest of which is the installation of additional external output capacitors. These output caps function as true filter elements and should be selected for bulk capacitance, low ESR and appropriate frequency response. All external capacitors should have appropriate voltage ratings and be located as close to the converter as possible. Temperature variations for all relevant parameters should also be taken carefully into consideration.

The most effective combination of external I/O capacitors will be a function of line voltage and source impedance, as well as particular load and layout conditions. Our Applications Engineers can recommend potential solutions and discuss the possibility of our modifying a given device's internal filtering to meet your specific requirements. Contact our Applications Engineering Group for additional details.

In Figure 3, the two copper strips simulate real-world pcb impedances between the power supply and its load. In order to minimize measurement errors, scope measurements should be made using BNC connectors, or the probe ground should be less than ½ inch and soldered directly to the fixture.

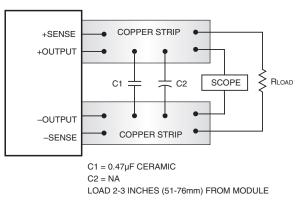


Figure 3. Measuring Output Ripple/Noise (PARD)

Floating Outputs

Since these are isolated DC/DC converters, their outputs are "floating" with respect to their input. Designers will normally use the –Output as the ground/ return of the load circuit. You can, however, use the +Output as ground/return to effectively reverse the output polarity.

Minimum Output Loading Requirements

UWE converters employ a synchronous-rectifier design topology and all models regulate within spec and are stable under no-load to full load conditions. Operation under no-load conditions however might slightly increase the output ripple and noise.

Thermal Shutdown

These UWE converters are equipped with thermal-shutdown circuitry. If environmental conditions cause the internal temperature of the DC/DC converter to rise above the designed operating temperature, a precision temperature sensor will power down the unit. When the internal temperature decreases below the threshold of the temperature sensor, the unit will self start. See Performance/ Functional Specifications.

Output Overvoltage Protection

UWE output voltages are monitored for an overvoltage condition via magnetic feedback. The signal is coupled to the primary side and if the output voltage rises to a level which could be damaging to the load, the sensing circuitry will power down the PWM controller causing the output voltages to decrease. Following a time-out period the PWM will restart, causing the output voltages to ramp to their appropriate values. If the fault condition persists, and the output voltage again climb to excessive levels, the overvoltage circuitry will initiate another shutdown cycle. This on/off cycling is referred to as "hiccup" mode.

Current Limiting

As soon as the output current increases to substantially above its rated value, the DC/DC converter will go into a current-limiting mode. In this condition, the output voltage will decrease proportionately with increases in output current, thereby maintaining somewhat constant power dissipation. This is commonly referred to as power limiting. Current limit inception is defined as the point at which the full-power output voltage falls below the specified tolerance. See Performance/Functional Specifications. If the load current, being drawn from the converter, is significant enough, the unit will go into a short circuit condition as specified under "Performance."

Short Circuit Condition

When a converter is in current-limit mode, the output voltage will drop as the output current demand increases. If the output voltage drops too low, the magnetically coupled voltage used to develop primary side voltages will also drop, thereby shutting down the PWM controller. Following a time-out period, the PWM will restart causing the output voltages to begin ramping to their appropriate values. If the short-circuit condition persists, another shutdown cycle will be initiated. This on/off cycling is referred to as "hiccup" mode. The hiccup cycling reduces the average output current, thereby preventing internal temperatures from rising to excessive levels. The UWE is capable of enduring an indefinite short circuit output condition.

Features and Options

On/Off Control

The input-side, remote On/Off Control function can be ordered to operate with either polarity:



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Positive-polarity models ("P" part-number suffix) are enabled when the On/Off Control is left open or is pulled high, as per Figure 4. Positive-polarity devices are disabled when the On/Off Control is pulled low.

Negative-polarity devices ("N" suffix) are off when the On/Off Control is open (or pulled high), and on when the On/Off Control is pulled low with respect to $-V_{IN}$ as shown in Figure 5.

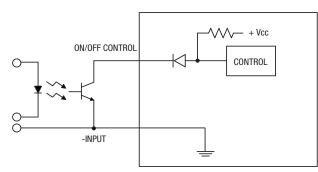


Figure 4. Driving the Positive Polarity On/Off Control Pin

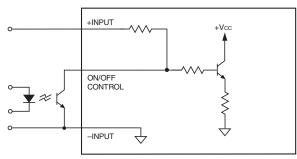


Figure 5. Driving the Negative Polarity On/Off Control Pin

Dynamic control of the remote on/off function is facilitated with a mechanical relay or an open-collector/open-drain drive circuit (optically isolated if appropriate). The drive circuit should be able to sink appropriate current (see Performance Specs) when activated and withstand appropriate voltage when deactivated. Applying an external voltage to the On/Off Control when no input power is applied to the converter can cause permanent damage to the converter.

Trimming Output Voltage

UWE converters have a trim capability that allows users to adjust the output voltages. Adjustments to the output voltages can be accomplished via a trim pot (Figure 6) or a single fixed resistor as shown in Figures 7 and 8. A single fixed resistor can increase or decrease the output voltage depending on its connection. The resistor should be located close to the converter and have a TCR less than 100ppm/°C to minimize sensitivity to changes in temperature. If the trim function is not used, leave the trim pin floating.

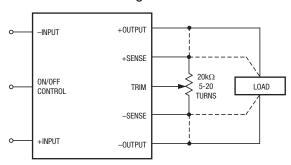


Figure 6. Trim Connections Using A Trimpot

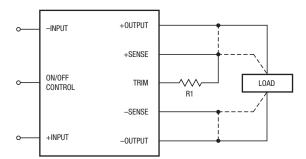


Figure 7. Trim Connections To Increase Output Voltages Using a Fixed Resistor

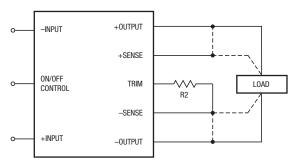


Figure 8. Trim Connections To Decrease Output Voltages

A single resistor connected from the Trim to the +Output, or +Sense where applicable, will increase the output voltage in this configuration. A resistor connected from the Trim to the –Output, or –Sense where applicable, will decrease the output voltage in this configuration.

Trim adjustments greater than the specified range can have an adverse affect on the converter's performance and are not recommended. Excessive voltage differences between Vour and Sense, in conjunction with trim adjustment of the output voltage, can cause the overvoltage protection circuitry to activate (see Performance Specifications for overvoltage limits). Power derating is based on maximum output current and voltage at the converter's output pins. Use of trim and sense functions can cause output voltages to increase, thereby increasing output power beyond the converter's specified rating or cause output voltages to climb into the output overvoltage region. Therefore:

(Vout at pins) x (lout) <= rated output power



Trim Equations Trim Up Trim Down 3.3 Volt Output $R_{T_{\text{DOWN}}}(k\Omega) = \frac{16.31}{3.3 - V_{\Omega}} -10.2$ 13.3(Vo-1.226) -10.2 $R_{T_{UP}}(k\Omega) = -$ Vo-3.3 **5 Volt Output** $R_{T_{UP}}(k\Omega) = \frac{20.4(Vo - 1.226)}{Vo - 5}$ $R_{T_{\text{DOWN}}}(k\Omega) = \frac{25.01}{5 - V_{\Omega}} - 10.2$ -10.212 Volt Output $R_{T_{\text{DOWN}}}(k\Omega) = \frac{60.45}{12 - V_0} - 10.2$ $R_{T_{UP}}(k\Omega) = \frac{49.6(Vo - 1.226)}{-10.2}$ Vo-12 24 Volt Output 124.2 101(Vo - 1.226) $R_{T_{DOWN}}(k\Omega) = \frac{124.2}{24 - V_{\Omega}}$ - -10.2 $R_{T_{IIP}}(k\Omega) = \cdot$ - -10.2 $V_0 - 24$

Note: Resistor values are in $k\Omega$. Adjustment accuracy is subject to resistor tolerances and factory-adjusted output accuracy. $V_0 =$ desired output voltage.

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Remote Sense Note: The Sense and Vout lines are internally connected through low value resistors. Nevertheless, if the sense function is not used for remote regulation the user should connect the +Sense to $+V_{OUT}$ and -Sense to $-V_{OUT}$ at the DC/DC converter pins.

UWE series converters have a sense feature to provide point of use regulation, thereby overcoming moderate IR drops in pcb conductors or cabling. The remote sense lines carry very little current and therefore require minimal cross-sectional-area conductors. The sense lines are used by the feedback control-loop to regulate the output. As such, they are not low impedance points and must be treated with care in layouts and cabling. Sense lines on a pcb should be run adjacent to dc signals, preferably ground. In cables and discrete wiring applications, twisted pair or other techniques should be implemented.

UWE series converters will compensate for drops between the output voltage at the DC/DC and the sense voltage at the DC/DC provided that:

 $[V_{OUT}(+) - V_{OUT}(-)] - [Sense(+) - Sense(-)] \le 5\%$ Vout

Output overvoltage protection is monitored at the output voltage pin, not the Sense pin. Therefore, excessive voltage differences between V_{OUT} and Sense in conjunction with trim adjustment of the output voltage can cause the overvoltage protection circuitry to activate (see Performance Specifications for overvoltage limits). Power derating is based on maximum output current and voltage at the converter's output pins. Use of trim and sense functions can cause output voltages to increase thereby increasing output power beyond the UWE's specified rating or cause output voltages to climb into the output overvoltage region. Also, the use of Trim Up and Sense combined may not exceed +10% of Vour. Therefore, the designer must ensure:

(Vout at pins) x (lout) ≤ rated output power

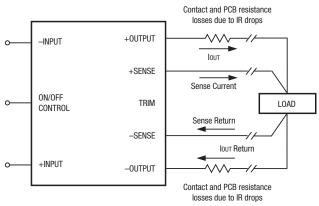
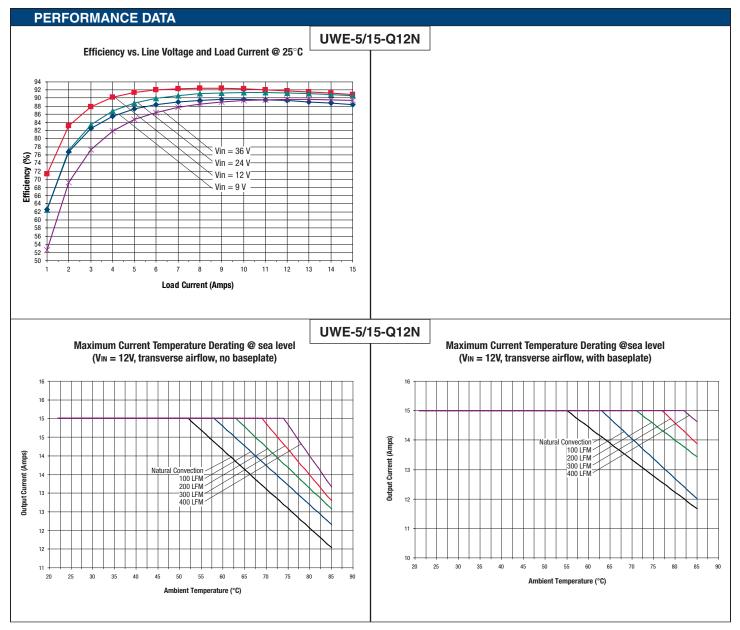


Figure 9. Remote Sense Circuit Configuration





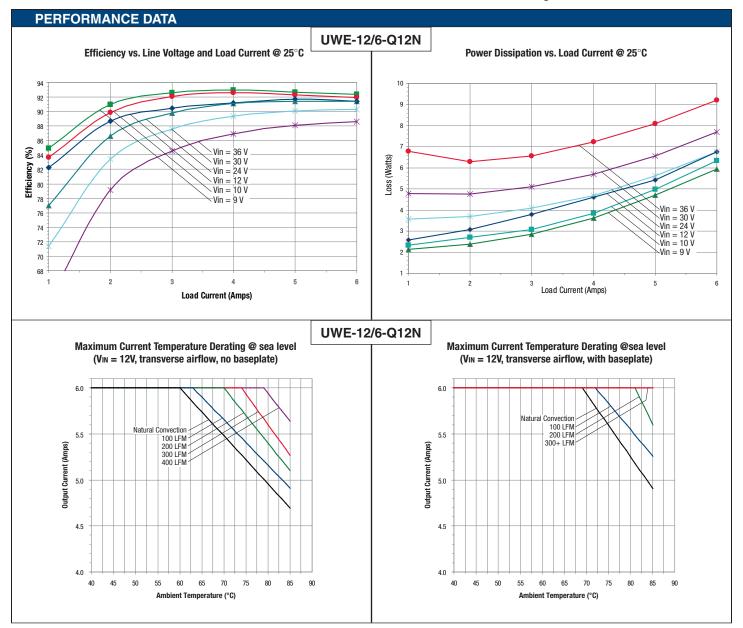
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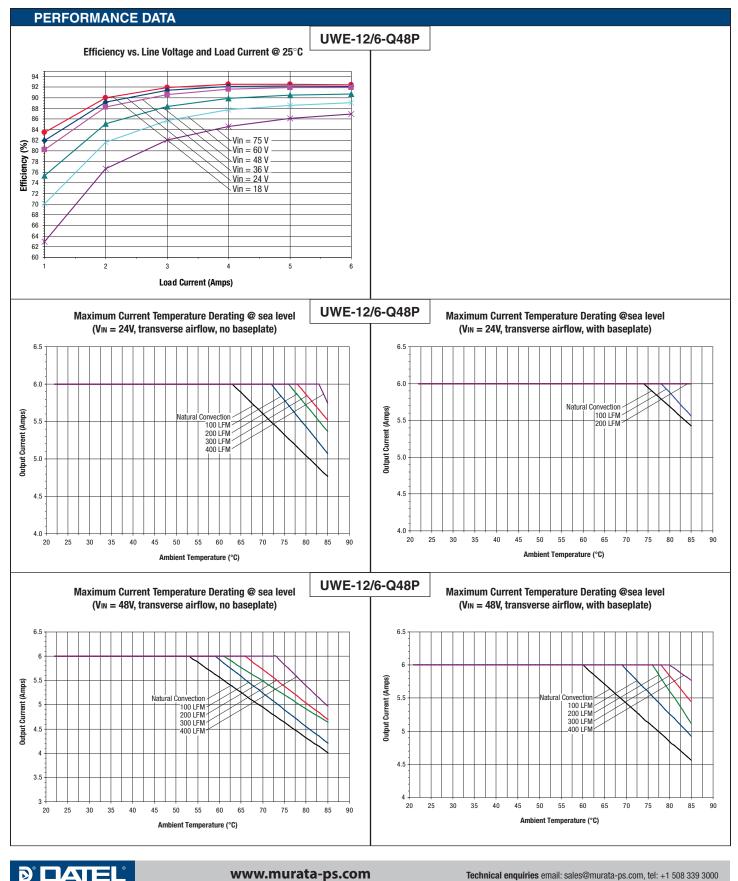
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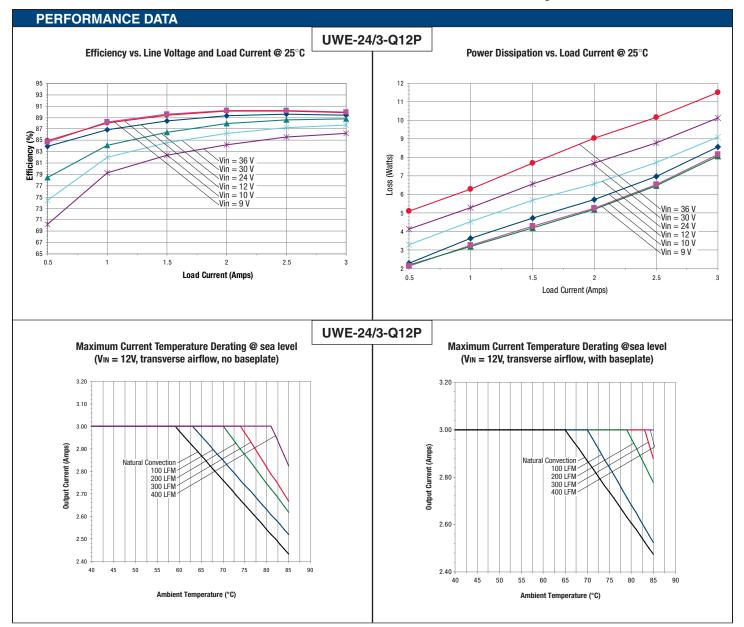
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