

• High resolution: 4.3 to 34.5nm (depends on the number of splits)

• High response speed: 400mm/s

• DC offset, gain, phase automatic conditioning

• 32 bit binary output by data request input (T14, T16, T17)

Unit: mm

-T15	BD95-T16	BD95-T10	BD95-T17
visions) or 8.6 nm(16 divisions) 100 nm, 50 or 10 nm during pitch compensation		8.6 nm (16divisions) or 4.3 nm(32 divisions) 100 nm, 50nm, or 5 nm during pitch compensation	
th 8 divisions)	120 mm/s (with 16 divisions)	mm/s (with 16 divisions) 120 mm/s (with 16 divisions) 60 mm/s (with 32 divisions)	
ompensation (compliant with EIA-422) AB quadrature 2 without pitch Jarm (compliant with EIA-422) (Switching between automatic reset and CALE signal (SIN/COS) 32-bit binary data (-T14, -T16 only)			
oken or disconnected) LEDs (Turn on independently for speed alarm and level alarm) alarm occurs. Switching between automatic reset and holding is possible			
only A round-off error of 1 resolution occurs.			
encies allowing compensation update: Input signals of 180 kHz or less			
DC + 24V ± 1V			
400mA (maximum)			
0°C to 50°C / 32° F to 122°F			
-10°C to 60°C / 14° F to 140°F			
(D)x32(H) mm/6.77"(W)x5.66"(D)x1.25"(H)			
Approx. 0.8 kg/Approx.1.76lbs			